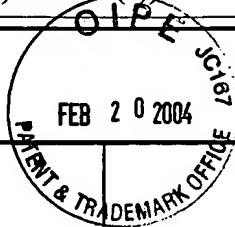


TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.

BUR920030149US1In Re Application Of: **Maynard et al.**

Serial No.

10/707,039

Filing Date

02/04/04

Examiner

Group Art Unit

Title:

IC DESIGN MODELING ALLOWING DIMENSION-DEPENDENT RULE CHECKING

Address to:

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450****37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
- ☐ the statement specified in 37 CFR 1.97(e);
- OR**
- ☐ the fee set forth in 37 CFR 1.17(p).

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IC DESIGN MODELING ALLOWING DIMENSION-DEPENDENT RULE CHECKING**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **09-0456 (IBM)** as described below.
- ☐ Charge the amount of _____
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Dated: **2/16/04****Richard M. Kotulak****Reg. No. 27,712**

CC:

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

BUR920030149US1

Application Number

Applicant(s)

Maynard et al.

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		"Critical Area Computation via Voronoi Diagrams," E. Papadopoulou et al., IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 18, No. 4, April 1999, pp. 463-474.
		"The Loo Voronoi Diagram of Segments and VLSI Applications," E. Papadopoulou et al., International Journal of Computational Geometry and Applications, Vol. 11, No. 5, 2001, pp. 503-528.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.